SR107 Output Example

Static Data Output Using LabWindows/CVI "VISA" Library A32 Functions

**** note: SR107 resides in SR192 slot DRA1 and will use FCNTL1 for Output Strobe**

	A32 Address ("Offset")	Data "Value" sent with "viOut16" function
1)	**40010** (SR192 VXI Function Code Register)	**1** (Function Code 1 for Out Enable/In Strobe Select)
2)	**200000** (SR192 slot DRA1)	**5800** (Always, Transparent, Channel 1)
3)	**200000** (SR192 slot DRA1)	**5801** (Always, Transparent, Channel 2)
4)	**200000** (SR192 slot DRA1)	**5802** (Always, Transparent, Channel 3)
5)	**200000** (SR192 slot DRA1)	**5803** (Always, Transparent, Channel 4)
6)	**200000** (SR192 slot DRA1)	**5804** (Always, Transparent, Channel 5)
7)	**200000** (SR192 slot DRA1)	**5805** (Always, Transparent, Channel 6)
8)	**200000** (SR192 slot DRA1)	**5806** (Always, Transparent, Channel 7)
9)	**200000** (SR192 slot DRA1)	**5807** (Always, Transparent, Channel 8)
10)	**200000** (SR192 slot DRA1)	**5808** (Always, Transparent, Channel 9)
11)	**200000** (SR192 slot DRA1)	**5809** (Always, Transparent, Channel 10)
12)	**200000** (SR192 slot DRA1)	**580A** (Always, Transparent, Channel 11)
13)	**200000** (SR192 slot DRA1)	**580B** (Always, Transparent, Channel 12)
14)	**200000** (SR192 slot DRA1)	**580C** (Always, Transparent, Channel 13)
15)	**200000** (SR192 slot DRA1)	**580D** (Always, Transparent, Channel 14)
16)	**200000** (SR192 slot DRA1)	**580E** (Always, Transparent, Channel 15)
17)	**200000** (SR192 slot DRA1)	**580F** (Always, Transparent, Channel 16)
18)	**40010** (SR192 VXI Function Code Register)	**8** (Function Code 8 for Power Management)
19)	**200000** (SR192 slot DRA1)	**0103** (CH 1-8 & 9-16 registered, PWRUP+ enabled)
20)	**40010** (SR192 VXI Function Code Register)	**9** (Function Code 9 for Output Strobe Select)
21)	**200000** (SR192 slot DRA1)	**0001** (FCNTL1- selected as Output Strobe)
22)	**40010** (SR192 VXI Function Code Register)	**0** (Function Code 0 for WRITE/READ Data)
23)	**200000** (SR192 slot DRA1)	**5F5F** (WRITE 0x5F5F to slot DRA1)

24) The data (0x5F5F) is now loaded into the SR107's output registers. FCNTL1- must now transition from "high" to "low" to strobe the data through the output registers to the output drivers.