

VXI Digital Test Solutions

High Performance

Variable Voltage

VXI Digital Test Modules

Talon Instruments is offering it's popular SR192 VXI Digital Test Module in standard pre-configured models at significant savings.

If your digital test requirements span a wide range of voltages and unique requirements the variable voltage models described here may be just the product you need. For the cost they offer the most power and flexibility of any VXI system on the market.

These new pre-configured models are capable of performing stimulus/response, parallel and serial bus emulation test functions. They include features such as:

- Timing Rates to 50MHz
- Data Rates to 25MHz (50MHz Multiplexed)
- 5ns Strobe Placement
- Variable voltage levels from +7V to -5V
- 2 Voltage Families assigned per 8 channels
- 25mA Source/Sink
- 5 Memory Architecture (Stimulus, Tristate, Record, Expect & Mask)
- 128K Bits Memory/Channel, (256K Multiplexed)
- Real-Time error capture
- Parallel and Serial bus emulation
- Data Formats (Hold,RTZ,RT0, & RTC)
- Conditional/Unconditional Jump, Go-sub Test Sequences
- 128K Test Sequence Memory
- VXI Plug&play Drivers
- Active X Graphic Software Development Routines

3 Models

SR192/64V

SR192/96V

SR192/V



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INSTRUMENTS

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Model Descriptions

The three Variable Voltage VXI models offered in standard configurations are housed in the SR192 motherboard which is a 2-slot "C" size VXI module. The SR101 timing generator(s) are utilized and the I/O modules are the SR214 bi-directional 16 channel modules. Each system includes the SR210 Accessory Module which provides a programmable clock source and the threshold level control for the SR214 I/O modules. The SR192/20 Development Environment software package for creating and editing test programs is also included with each model. Models SR192/64TS and SR192/96TS may be field upgraded at a later date with a second timing generator and additional I/O modules.

- **SR192/64V** provides 64 bidirectional channels of variable voltage (+7 to -5Vdc) digital stimulus/response and a single timing generator.
- **SR192/96V** provides 96 bidirectional channels of variable voltage (+7 to -5Vdc) digital stimulus/response and a single timing generator.
- **SR192/V** provides 192 bidirectional channels of variable voltage (+7 to -5Vdc) stimulus/response and 2 timing generators. The 192 channels may be programmed to function as a single test system or as two fully independent 96 channel systems.

SR214 Digital I/O Module

The SR214 is a 16 channel I/O module designed to be installed as 1 of 12 daughter boards in a Model SR192 VXI Digital Test Module. The following specifications apply:

General

16 Bi-directional I/O Channels
128K Bits per channel, (256K Multiplex Mode)
25MHz data rate
Parallel and Serial Operating Modes
Data Formats (Hold, RTZ, RT0, & RTC)
<5ns skew

Electrical

Output

High Level +7 to -5V
Low Level +7 to -5V
Source/Sink Current 50mA
Voltage Swing 12V
Output Impedance 80Ω
Short Circuit Protected

Input

High Threshold +7 to -5V
Low Threshold +7 to -5V
Input Impedance >30KΩ

SR210 Accessory Module

The SR210 is installed in its own daughter board slot and provides internal and external clock sources as well as the reference control for the SR214 I/O modules.

Specifications

Internal Clocks 2
External Clocks 3
Range 100Hz to 50MHz
Resolution 0.029 Hz
Output Levels 2 @ TTL
1 @ +8 to -8 V
Voltage Families 2
Threshold Resolution 10mV

SR101 Timing Generator

One or two SR101 timing generators may be installed in an SR192 motherboard. Each SR101 will control up to 6 I/O modules.

The function of the SR101 timing generator is to provide the timing and control for the SR192 when executing a test or emulating a bus interface. The SR101 generates timing signals to the UUT, tests signals from the UUT, generates the required memory address and control signals for the I/O modules, and samples the results from the I/O modules.

Specifications

Clocks

Internal	1
External	2 Front Panel
Maximum Frequency	50MHz
External Input Levels	TTL
External Input Impedance	82Ω to ground

Output Signals

TSOUT1-5	Qty. 5
Minimum Period	20 nsec
Output Levels	TTL
Output Impedance	<50Ω

Input Signals

TSINP1-2	Qty. 2
Input Levels	TTL
Input Impedance	82Ω to ground

Test Sequencing

Test Sequence Memory	128K
Conditional & Unconditional Looping	Jump, Go-sub 32K

SR192/20

The Development Environment is an Active X editor used to generate and edit table data, timing data and create test sequences. It is an interactive 32-bit Windows application which aids the test engineer in generating and testing his test programs. It shortens the test development time significantly and is compatible with most test languages.